| L Number | Hits | Search Text | DB | Time stamp |
|---|---|---|---|---|
| 1 | 1526 | 711/103,206,153.ccls. | USPAT | 2004/03/26 18:20 |
| 5 | 281 | (page adj table) and 711/103,206,153.ccls. | USPAT | 2004/03/26 19:22 |
| 8 | 1 | (page adj table) and (predefined same attribute) and (memory adj management) and 711/103,206,153.ccls. | USPAT | 2004/03/26 19:24 |
| 7 | 53 | (page adj table) and attribute and (memory adj management) and 711/103,206,153.ccls. | USPAT | 2004/03/26 19:34 |
| 14 | 0 | "level-one page table" | USPAT | 2004/03/26 19:46 |
| - | 0 | 395/186.ccls. | USPAT | 2004/03/26 19:20 |
| - | 0 | 395/$.ccls. | USPAT | 2004/03/23 02:18 |
| - | 32 | 380/225.ccls. | USPAT | 2004/03/23 02:23 |
| - | 0 | hardware and (SPU or (secure adj processing adj unit)) and 380/225.ccls. | USPAT | 2004/03/23 02:21 |
| - | 15 | hardware and (SPU or (secure adj processing adj unit)) and 380/$.ccls. | USPAT | 2004/03/23 02:23 |
| - | 123 | 713/164.ccls. | USPAT | 2004/03/23 02:23 |
| - | 3 | hardware and (SPU or (secure adj processing adj unit)) and 713/164.ccls. | USPAT | 2004/03/23 02:24 |
| - | 31 | hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls. | USPAT | 2004/03/23 02:27 |
| - | 0 | "level-one page table" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls. | USPAT | 2004/03/26 19:46 |
| - | 0 | "page table" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls. | USPAT | 2004/03/23 02:29 |
| - | 19 | "tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls. | USPAT | 2004/03/23 02:34 |
| - | 0 | logic and (security adj register) and (cryptographic adj certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 06:53 |
| - | 0 | logic and (register) and (cryptographic near certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 02:36 |
| - | 0 | (register) and (cryptographic near certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 02:37 |
| - | 4 | (register) and (cryptographic same certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 06:54 |
| - | 0 | (security adj register) and (cryptographic adj certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 06:53 |
| - | 4 | table and (register) and (cryptographic same certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 06:56 |
| - | 2 | logic and table and (register) and (cryptographic same certificate) and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 07:11 |
| - | 19 | cryptographic and ("tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 713/$.ccls.) | USPAT | 2004/03/23 07:02 |
| - | 1 | cryptographic and "tamper resistant" and hardware and (SPU or (secure adj processing adj unit)) and 6427140.pn. | USPAT | 2004/03/23 07:02 |
| - | 1 | memory and 6658568.pn. | USPAT | 2004/03/23 07:12 |
| - | 1 | memory and 6427140.pn. | USPAT | 2004/03/23 07:44 |
| - | 1 | "internal memory" and processor and 6427140.pn. | USPAT | 2004/03/24 21:51 |
| - | 1 | tamper and logic and 6427140.pn. | USPAT | 2004/03/24 19:04 |

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|---|---|---|---|---|
| – | 1 | interface and 6427140.pn. | USPAT | 2004/03/24 15:43 |
| – | 1 | "memory management unit" and 6427140.pn. | USPAT | 2004/03/23 17:26 |
| – | 1 | security same register and 6427140.pn. | USPAT | 2004/03/24 17:43 |
| – | 1 |  register and 6427140.pn. | USPAT | 2004/03/25 21:59 |
| – | 1 | battery and 6427140.pn. | USPAT | 2004/03/24 19:20 |
| – | 1 | ((encrypt$ or cryptograph$) near key same nvram) and 6427140.pn. | USPAT | 2004/03/24 19:21 |
| – | 1 | (identifier same process$) and 6427140.pn. | USPAT | 2004/03/24 21:52 |
| – | 1 | (identifier with process$) and 6427140.pn. | USPAT | 2004/03/24 21:54 |
| – | 1 | SPU and 6427140.pn. | USPAT | 2004/03/25 17:02 |
| – | 1 | (SPU same process$) and 6427140.pn. | USPAT | 2004/03/24 22:20 |
| – | 1 | ((id or identifier) same SPU same process$) and 6427140.pn. | USPAT | 2004/03/24 22:27 |
| – | 1 | ((id or identifi$) same SPU same process$) and 6427140.pn. | USPAT | 2004/03/24 23:02 |
| – | 1 | access same register and 6427140.pn. | USPAT | 2004/03/24 23:02 |
| – | 1 | bus and 6427140.pn. | USPAT | 2004/03/25 17:30 |
| – | 1 | appliance and 6427140.pn. | USPAT | 2004/03/25 17:16 |
| – | 1 | bus and (together or both) and 6427140.pn. | USPAT | 2004/03/25 17:43 |
| – | 1 | "set-top" and 6427140.pn. | USPAT | 2004/03/25 19:50 |
| – | 1 | (primary or main) and 6427140.pn. | USPAT | 2004/03/25 19:59 |
| – | 1 | (address or critical or register) and (access adj control) and 6427140.pn. | USPAT | 2004/03/25 23:04 |
| – | 0 | (processor with (address or critical or register)) and (access adj control) and 6427140.pn. | USPAT | 2004/03/25 23:05 |
| – | 1 | (processor same (address or critical or register)) and (access adj control) and 6427140.pn. | USPAT | 2004/03/25 23:31 |
| – | 1 | (process$ with (secur$ with register)) and (access adj control) and 6427140.pn. | USPAT | 2004/03/26 00:09 |
| – | 1 | 6,292,569 .pn. | USPAT | 2004/03/26 00:09 |
| – | 1 | 6,567,974 .pn. | USPAT | 2004/03/26 00:09 |